


AMENDMENTS TO THE DRAWINGS

New drawings are appended to the response as directed by the Examiner.

AMENDMENTS TO THE SPECIFICATION

On pages 1 and 2, kindly replace numbered references as follows:

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1. United States patent application serial number 09/811,194 entitled, "Redundant, High-Availability Storage System" (~~HP Docket No. 10003435-1~~), naming Anthony J. Benson and James J. deBlanc as inventors and filed on even date within herewith.
 2. United States patent application serial number 09/810,965 entitled, "~~System and Method for~~ Data Corruption Avoidance on a Backplane Bus Adapted to Receive Bus Controller Cards of Different Types" (~~HP Docket No. 10003436-1~~), naming Anthony J. Benson and Patrick McGoeys as inventors and filed on even date within herewith.
 3. United States patent application serial number 09/811,193 entitled, "Multiple-Path Interface Card for Interfacing Multiple Isolated Interfaces to a Storage System" (~~HP Docket No. 10003437-1~~), naming Anthony J. Benson and James J. deBlanc as inventors and filed on even date within herewith.
 4. United States patent application serial number 09/811,192 entitled, "Circuit for Switching One or More HVD Transceivers" (~~HP Docket No. 10003439-1~~), naming Anthony J. Benson as inventor and filed on even date within herewith.
 5. United States patent application serial number 09/810,963 entitled, "Management of Communication Bus Resets" (~~HP Docket No. 10005621-1~~), naming Anthony J. Benson, James L. White, and Dovard K. Howard as inventors and filed on even date within herewith.
 6. United States patent application serial number 09/811,196 entitled, "~~Master Slave~~ Communication Bus Controllers Controller Including Designation of Primary and Secondary Status According to Slot Position" (~~HP Docket No. 10013519-1~~),

Q1 naming Anthony J. Benson,, James L. White, and Dovard K. Howard as inventors and filed on even date ~~within~~ herewith.

7. United States patent ~~application~~ number 6,567,879 entitled, "Management of Resets for Interdependent Dual Small Computer Standard Interface (SCSI) Bus Controllers" (~~HP Docket No. 10992797-1, PTO Serial No.: 09/605,161, Anthony J. Benson, et al., filed on June 27, 2000.~~

Q2 On page 4, replace the paragraph starting at line 16 with the following paragraph:

Referring to FIG. 1, a schematic block diagram of a backplane 100 is shown. The backplane 100 is ~~preferably~~ can be a printed circuit board that may be utilized as a component within another assembly, such as a mass storage unit. A first bus 108 and a second bus 110 are is implemented on the backplane 100, ~~preferably~~ as standard SCSI buses. However, the buses 108, 110 may be another type of bus, if desired. The first bus 108 ~~preferably~~ includes a number of ports 104, each ~~preferably~~ having the same physical configuration. The ports 104 each are adapted to connect to a peripheral device, such as a disk drive. The second bus 110 ~~preferably~~ includes a number of ports 106, each ~~preferably~~ having the same physical configuration. The ports 106 each are adapted to connect to a peripheral device, such as a disk drive.

Q3 On page ~~8~~, replace the paragraph starting at line 15 with the following paragraph:

Referring as well to Fig. 3, a schematic of an end view 1 of the card 200 is shown. The first ~~host~~ backplane connector ~~206~~ 202 is positioned adjacent to the second ~~host~~ backplane connector ~~214~~ 204. However, other configurations and relative positions of the ~~host~~ backplane connectors ~~206, 214~~ 202, 204 are possible. The card 200 also includes a panel switchbox 300. The panel switchbox 300 preferably includes a first DIP switch 302, a second DIP switch 304, a third DIP switch 306, a fourth DIP switch 308, and a fifth DIP switch 310. However, other types of switches may be used if desired, and additional switches may be provided if desired. Preferably, the panel switchbox 300 is located on the same end of the card 200 as the ~~host~~ backplane connectors ~~206, 214~~ 202, 204, such that the DIP switches 302-

Q3

310 are facing outward from and accessible from that end of the card 200. The functions of the DIP switches 302-310 are shown in Table 1.

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